


## Preface

This volume contains the selection of papers presented at SMT 2007, the  
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EDFAC). The workshop was held in Berlin, Germany, on July 1–2, 2007, affiliated with the 19th International Conference on Computer-Aided Verification (CAV 2007).

The aim of the workshop was to bring together researchers working on SMT and users of SMT techniques, and to provide a forum for discussing new decision procedures, new theories of interest, combinations of decision procedures, novel implementation techniques, benchmarks and evaluation methodologies, and theoretical results related to SMT.

The final program included

- invited talks by Peter O’Hearn (Queen Mary, University of London) and Rupak Majumdar (University of California, Los Angeles).
- 9 technical paper presentations (4 “presentation-only”)
- a panel on the SMT-LIB language
- a session dedicated to the SMT competition (SMT-COMP), where entrants briefly described their tools

The submitted papers were evaluated by members of the program committee, whose help is gratefully acknowledged:

Clark Barrett	New York University
Alessandro Cimatti	ITC-Irst, Trento
Byron Cook	Microsoft Research
Amit Goel	Intel Corporation
Aarti Gupta	NEC Labs America

Sava Krstić	Intel Corporation
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